



M185XW01 V0

AU OPTRONICS CORPORATION

(V)	Preliminary Specification
()	Final Specification

90101 11 1 E0B	
5XW01 V0	
	Color TFT-LCD 5XW01 V0

Customer	Date
Approved by	
Note: This Specificatio change without r	

Approved by	Date
Prepared by	
	0000/00/40
<u>Chiyinwu</u>	2008/06/13
Desktop Display E AU Optronics	





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Record of Revision

Version and Date	Pag e	Old description	New Description	Remarl
0.1 2008/03/10	All	First Edition for Customer	N/A	
0.2 2008/06/02	All	old format	new format	
	5	 Display Characteristics No screen diagnoal description weight 1900(max) TDB No temperature range 	2.1 Display Characteristics 1. add screen diagnoal : 470.1 mm (18.51") 2. weight 2000 (max) 3. add temperature range	
	6	View angel	Storage (shipping) -20 to +60 [°C]	
	10	1. I/F PCB interface FI-XB30SSRL-HF16 (JAE) / MSCKT2407P30B (信盛) 2. Lamp connector CVILUX CP0502SL090 / YEON HO 35001HS-02L Mating Type: 35001WR-02LP	1. I/F PCB Interface: FI-XB30SSRL-HF16(JAE)/MSCKT2407P30HB(信盛) 2. Lamp connector Deleted	
	14		Parameter Min Typ Max Units Condition	
		5.1.2 Signal Electrical Characteristics Parameter Min Typ Max Units Condition	Differential Input High Threshold - 50 100 ImV 1.2V Note 1	
Differential legal High Threathold - 100 [mV]			Differential Input Common Mode Voltage	
	(1)		(1) Parameter Min. Typ. Max. Unit Note CCFL Ignition Voltage	
		Parameter Min. Typ. Max. Unit Condition	ViOFL, Ta= DTC)	
		(2) Note 8: The CCFL can work normally if the PWM Dimming ratio range is from 20% to 100% and the lamp current is 7.5mA	11 12.1 [Watt] Note 2: CCFL standard current is measured by continue mode inverter at 25±2°C.	





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	21	6.4.1 Timing Characteristics	Signal	Item	Symbol	Min	Тур	Max	Unit
	- ·	0.4.1 Tilling Onardotonstros		Period	Th	1480	1606	2047	Telk
		Signal Item Symbol Min Typ Max Unit Period Th 1406 1606 2047 Tclk	H-section	Active	Tdisp(h)	1366	1366	1366	Telk
		H-section Active Tdisp(h) 1366 1366 1366 Telk	Note: Clear Frances	Blanking	Tblk(h)	114	240	681	Telk
		Blacking Tblk(h) 40 240 681 Tclk Note : The maximum Clock fequency under 90 MHz	Note2 : Clock Frequer	icy guivinz(iviax.)=	1460(H)*610(V	1-10HZ			
	24	7.1 TFT LCD Module	Connector Name	/ Designation	n Ir	iterface Co	nnector /	Interface	card
		Connector Name / Designation Interface Connector / Interface card			F	-XB30SSRL	-		
			Type Part Number		ľΗ	F16(JAE)/M	SCKT2407	P30HB(STI	vf)
. <u> </u>		Type Part Number FI-XB30SSRL-HF16 (AE) / MSCKT2407P30B (STM)			<u>'</u>			,	
	25	7.2 Backlight Unit	Connector Name / Des			acklight lamp			
			Manufacturer	YEON	но				
		Connector Name / Designation Lamp Connector / Backlight lamp Manufacturer CVILUX or YEON HO	Type Part Number	35001	HS-02L				
			Mating Type Part Numb	Mating Type Part Number 35001WR-					
		Type Part Number CP0502SL090/35001HS-02L							
		Mating Type Part Number CP0502P1ML0/35001WR-02LP							
		,							
	27	9.0 Shipping Label			AU Optronics MADE IN XXXXXXX (X	201 V.0 EZD4356 (1)	Rouss		
	28,2 9	2D drawing –PCBA	2D drawing	-change	PCBA	size			
0.3	21	6.4.1 Timing Characteristics	Signal	Item Period	Symbo Th	1416	Typ Max 1606 2047	Tdk	
2008/6/13		Signal Item Symbol Min Typ Max Unit	Note2 : Clock Frequency 90Mr	Active Blanking -tz(Max.)= 1416(H)*847	Tdisp(l Tblk(l: (V)*75Hz		1366 1366 240 681	Tdk Tdk	







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1.0 Handling Precautions

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- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- Be sure to turn off power supply when inserting or disconnecting from input connector. 2)
- Wipe off water drop immediately. Long contact with water may cause discoloration or spots. 3)
- When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth. 4)
- Since the panel is made of glass, it may break or crack if dropped or bumped on hard 5) surface.
- Since CMOS LSI is used in this module, take care of static electricity and insure human earth 6) when handling.
- Do not open or modify the Module Assembly. 7)
- Do not press the reflector sheet at the back of the module to any directions. 8)
- In case if a Module has to be put back into the packing container slot after once it was taken 9) out from the container, do not press the center of the CCFL reflector edge. Instead, press at the far ends of the CCFL Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Cold cathode fluorescent lamp in LCD contains a small amount of mercury. Please follow local ordinances or regulations for disposal.
- 13) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 14) The LCD module is designed so that the CCFL in it is supplied by Limited Current Circuit (IEC60950 or UL1950). Do not connect the CCFL in Hazardous Voltage Circuit.





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2.0 General Description

This specification applies to the 18.5 inch-wide Color a-Si TFT-LCD Module M185XW01. The display supports the WXGA - 1366(H) x 768(V) screen format and 16.7M colors (RGB 6-bits + Hi-FRC data). All input signals are 1-channel LVDS interface and this module doesn't contain an inverter board for backlight.

2.1 Display Characteristics

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	470.1(18.51")
Active Area	[mm]	409.8 (H) x 230.4 (V)
Pixels H x V		1366(x3) x 768
Pixel Pitch	[um]	300 (per one triad) × 300
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN Mode, Normally White
White Luminance (Center)	[cd/m ²]	300 cd/m ² (@ 7.5mA (Typ.)
Contrast Ratio		1000 (Typ.)
Optical Response Time	[msec]	5ms (Typ., on/off)
Nominal Input Voltage VDD	[Volt]	+5.0 V (Typ)
Power Consumption	[Watt]	17 W (Typ.)
(VDD line + CCFL line)		(without inverter, all black pattern)
Weight	[Grams]	2000 (Max)
Physical Size	[mm]	430.37 (W) x 254.6 (H) Typ. x 16.5 (D) Max
Electrical Interface		One channel LVDS
Support Color		16.7M colors (RGB 6-bit + Hi_FRC)
Surface Treatment		Anti-Glare, 3H
Temperature Range		
Operating	[°C]	0 to +50
Storage (Shipping)	[°C]	-20 to +60
RoHS Compliance		RoHS Compliance



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2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25 $^{\circ}\text{C}$:

ltem	Unit	Conditions	Min.	Тур.	Max.	Note
Viewing Angle	[dograp]	Horizontal (Right) CR = 10 (Left)	150	170	-	_
Viewing Angle	[degree]	Vertical (Up) CR = 10 (Down)	140	160	-	2
Contrast ratio		Normal Direction	600	1000	-	3
		Raising Time (T _{rR})	-	3.6	5.7	
Response Time	[msec]	Falling Time (T _{rF})	-	1.4	2.3	4
		Raising + Falling	-	5	8	
		Red x	0.618	0.648	0.678	
		Red y	0.309	0.339	0.369	
Color / Chromaticity		Green x	0.262	0.292	0.322	
Coordinates (CIE)		Green y	0.573	0.603	0.633	5
		Blue x	0.113	0.143	0.173	3
		Blue y	0.040	0.070	0.100	
		White x	0.283	0.313	0.343	
Color Coordinates (CIE) White		White y	0.299	0.329	0.359	
Central Luminance	[cd/m ²]		240	300	-	6
Luminance Uniformity	[%]		75	80	-	7
Crosstalk (in 60Hz)	[%]				1.5	8
Flicker	dB				-20	9



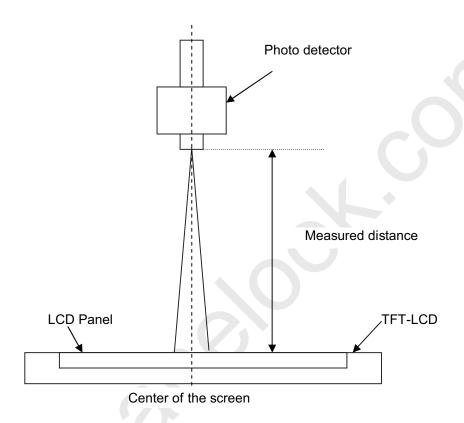


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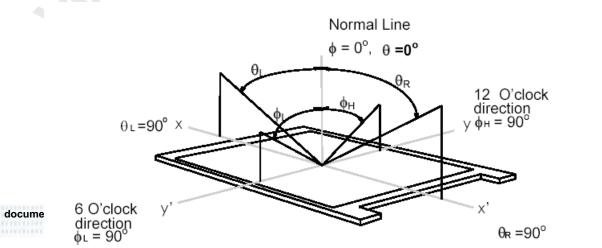
Note 1: Measurement method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring (at surface $35\,^{\circ}$ C). In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.



Note 2: Definition of viewing angle measured by ELDIM (EZContrast 88)

Viewing angle is the measurement of contrast ratio ≥ 10 , at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° (θ) horizontal left and right and 90° (Φ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.





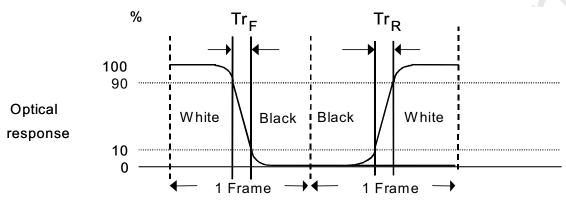
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Note 3: Contrast ratio is measured by TOPCON SR-3

Note 4: Definition of Response time measured by Westar TRD-100A

The output signals of photo detector are measured when the input signals are changed from "Full Black" to "Full White" (rising time, Tr_R), and from "Full White" to "Full Black" (falling time, Tf_F), respectively. The response time is interval between the 10% and 90% (1 frame at 60 Hz) of amplitudes.

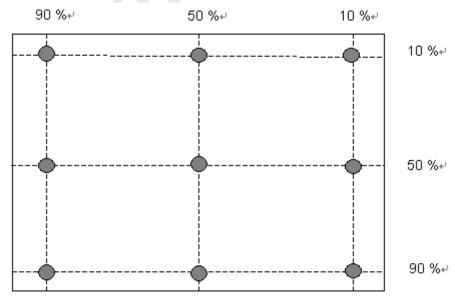


 $Tr_R + Tf_F = 5$ msec (typ.).

Note 5: Color chromaticity and coordinates (CIE) is measured by TOPCON SR-3

Note 6: Central luminance is measured by TOPCON SR-3

Note 7: Luminance uniformity of these 9 points is defined as below and measured by TOPCON SR-3



Uniformity = $\frac{\text{Minimum Luminance in 9 points (1-9)}}{\text{Maximum Luminance in 9 Points (1-9)}}$

Note 8: Crosstalk is defined as below and measured by TOPCON SR-3





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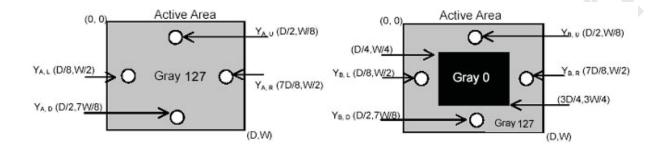
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$$CT = | YB - YA | / YA \times 100 (\%)$$

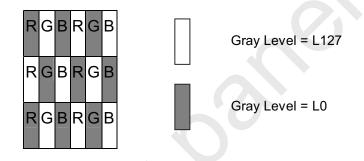
Where

YA = Luminance of measured location without gray level 0 pattern (cd/m2)

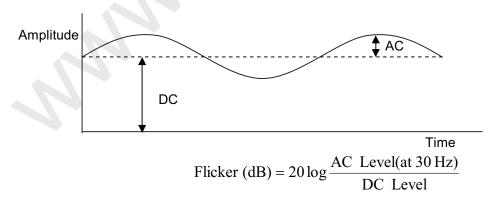
YB = Luminance of measured location with gray level 0 pattern (cd/m2)



Note 9: Test Patern: Subchecker Pattern measured by TOPCON SR-3



Method: Record dBV & DC value with TRD-100





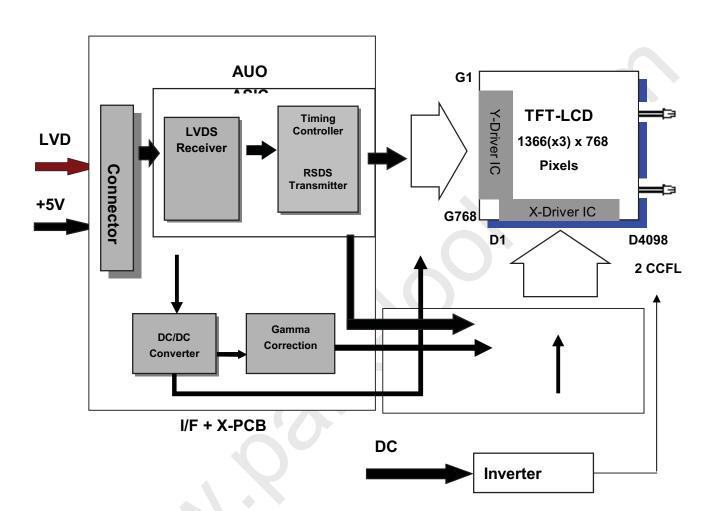


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3.0 Functional Block Diagram

The following diagram shows the functional block of the 18.5 inch Color TFT-LCD Module:



I/F PCB Interface:

FI-XB30SSRL-HF16(JAE)/MSCKT2407P30HB(信盛)

Mating Type:

FI-X30HL (JAE)(Locked Type)



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4.0 Absolute Maximum Ratings

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Absolute maximum ratings of the module are as following:

4.1 TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	4.5	+5.5	[Volt]	Note 1,2

4.2 Backlight Unit

Item	Symbol	Min	Max	Unit	Conditions
CCFL Current	ICFL	3.0	8	[mA] rms	Note 1,2

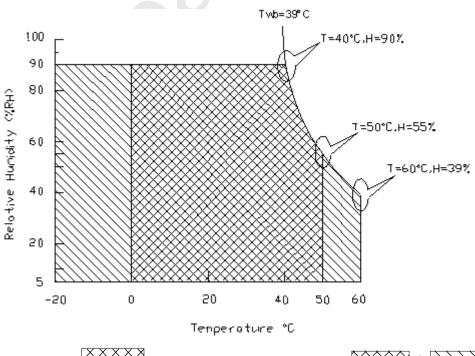
4.3 Absolute Ratings of Environment

Item	Symbol	Min.	Max.	Unit	Conditions
Operating Temperature	TOP	0	+50	[°C]	
Operation Humidity	HOP	5	90	[%RH]	Nata 2
Storage Temperature	TST	-20	+60	[°C]	Note 3
Storage Humidity	HST	5	90	[%RH]	

Note 1: With in Ta $(25^{\circ}C)$

Note 2: Permanent damage to the device may occur if exceeding maximum values

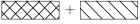
Note 3: For quality perfermance, please refer to AUO IIS(Incoming Inspection Standard).



Operating Range



Storage Range







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5.0 Electrical characteristics

5.1 TFT LCD Module

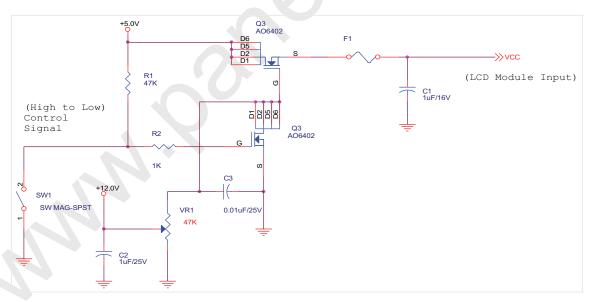
5.1.1 Power Specification

Input power specifications are as following:

Symbol	Parameter	Min	Тур	Max	Unit	Conditions
VDD	Logic/LCD Drive Voltage	4.5	5.0	5.5	[Volt]	+/-10%
IDD	Input Current	-	1.2	1.5	[A]	VDD= 5.0V, All Black Pattern At 60Hz,
PDD	VDD Power	-	6	8.25	[Watt]	VDD= 5.0V, All Black Pattern At 60Hz
IRush	Inrush Current	-	-	3	[A]	Note 1
VDDrp	Allowable Logic/LCD Drive Ripple Voltage	-	-	200	[mV] p-p	VDD= 5.0V, All Black Pattern At 60Hz

Note 1: Measurement conditions:

The duration of rising time of power input is 470us.



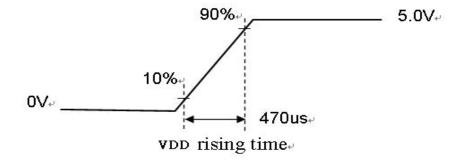
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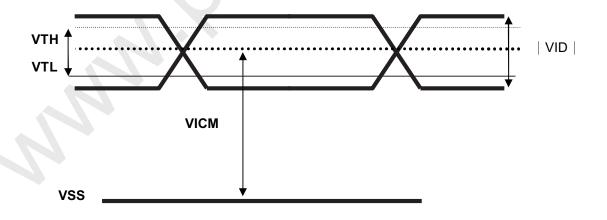
5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off. Please refer to specifications of SN75LVDS82DGG (Texas Instruments) in detail.

Characteristics of each signal are as following:

Symbol	Parameter	Min	Тур	Max	Units	Condition
VTH	Differential Input High	_	+50	+100	[mV]	VICM = 1.2V
VIII	Threshold	i	+30	+100	[IIIV]	Note 1
\	Differential Input Low	100	FO		[100] [1	VICM = 1.2V
VTL	Threshold	-100	-50		[mV]	Note 1
VID	Input Differential Voltage	100	400	600	[mV]	Note 1
VIOM	Differential Input Common	.4.0	14.0	. 4 4	D /3	VTH-VTL = 200MV (max)
VICM	Mode Voltage	+1.0	+1.2	+1.4	[V]	Note 1

Note 1: LVDS Signal Waveform





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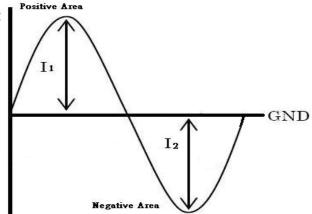
5.2 Backlight Unit

Parameter guideline for CCFL Inverter is under stable conditions at 25 °C (Room Temperature):

Parameter	Min.	Тур.	Max.	Unit	Note
CCFL Standard Current (ISCFL)	7.0	7.5	8.0	[mA] rms	
CCFL Operation Current (IRCFL)	3.0	7.5	8.0	[mA] rms	2
CCFL Frequency (FCFL)	40	50	80	[KHz]	3, 4
CCFL Ignition Voltage (ViCFL, Ta= 0°C)	1650	-	-	[Volt] rms	5
CCFL Ignition Voltage (ViCF, Ta= 25°ℂ)	1250	-	-	[Volt] rms	3
CCFL Operation Voltage (VCFL)	-	700 (@7.5mA)	880	[Volt] rms	6
CCFL Power Consumption (PCFL)	-	11	12.1	[Watt]	6
CCFL Life Time (LTCFL)	40,000	50,000) -	[Hour]	7

Note 1: Typ. values are AUO recommended design values.

- *1 All of characteristics listed are measured under the condition using the AUO test inverter.
- *2 It is recommended to check the inverter carefully. Sometimes, interfering noise stripes appear on the screen, and substandard luminance or flicker at low power may happen.
- *3 While designing an inverter, it is suggested to check safety circuit very carefully. Impedance of CCFL, for instance, becomes more than 1 [M ohm] when CCFL is damaged.
- *4 Generally, CCFL has certain delay time after applying kick-off voltage. It is recommended to keep on applying kick-off voltage for 1 [Sec] until discharge.
- *5 Reducing CCFL current will increase CCFL discharge voltage and generally increases CCFL discharge frequency. So all the parameters of the inverter should be carefully designed so the inverter will not produce too much leakage current from high-voltage output.
- *6 For designing CCFL current, it is highly recommanded to use symmetric and consistent sinusoidal wave for each CCFL input current with asymmetric ration of 10% or less in both postive area and negative area (ie. $0.9*\sqrt{2*I_{rms}} < I_1 \& I_2 < 1.1*\sqrt{2*I_{rms}}$) as refer to the following diagram, otherwise proper CCFL functionality cannot be guarantied.







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- **Note 2:** CCFL standard current is measured by continue mode inverter at $25\pm2^{\circ}$ C.
- **Note 3:** CCFL discharge frequency should be carefully determined to avoid interference between inverter and TFT LCD.
- **Note 4:** The frequency range will not affect lamp life and reliability characteristics.
- **Note 5:** CCFL inverter should be able to release power that has generating capacity exceeding 1650 volt. Lamp units need minimum voltage, 1650 Volt, for ignition.
- **Note 6:** The variance of CCFL power consumption is $\pm 10\%$. (IRCFL × VCFL × **2** = PCFL)
- **Note 7:** Definition of life time: brightness becomes 50%. The minimum life time of CCFL unit is on the condition of 7.5mA CCFL current and $25\pm2^{\circ}$ C.





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6.0 Signal Characteristic

6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.

	1	2		1365	1366
1st Line	R G	B R G E	3	R G	B R G B
			:		
			· · · · ·	•	
			:	•	•
	•	•	•	•	
768 Line	R G	B R G E	3	R G I	B R G B



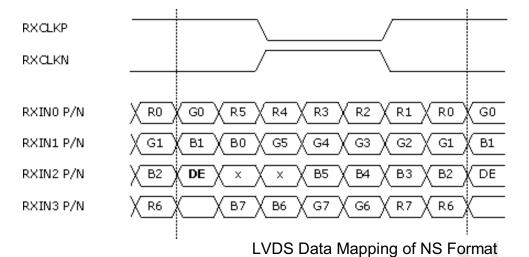


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6.2 The input data format

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8 BIT COLOR BIT ORDER			6 B COL BIT C	
MSB R7				
	R6			
	R5		MSB	R5
	R4			R4
	R3			R3
	R2			R2
	R1			R1
LSB	RO		LSB	RO

RXCLKP RXCLKN R2 R7 R6 R5 R4 R3 R2 G2 RXINO P/N G2 RXIN1 P/N G3 ВЗ В2 G7 G6 G5 G4 G3 ВЗ RXIN2 P/N В6 В4 DE В4 DE В7 В5 RXIN3 P/N RO В1 В0 G1 G0 R1 R0

8 BIT COLOR BIT ORDER			001	SIT LOR ORDER
MSB	R7		MSB	R7
	R6			R6
	R5			R5
	R4			R4
	R3			R3
	R2		LSB	R2
	R1			
LSB	RO			

LVDS Data Mapping of JEIDA Format

Note1: Normally, DE, VS, HS on EVEN channel are not used.

Note2: 8-bits signal input.



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6.3 Signal Description

The module using one LVDS receiver SN75LVDS82(Texas Instruments). LVDS is a differential signal technology for LCD interface and high speed data transfer device. LVDS transmitters shall be SN75LVDS83(negative edge sampling). The first LVDS port(RxOxxx) transmits odd pixels while the second LVDS port(RxExxx) transmits even pixels

PIN#	SIGNAL NAME	DESCRIPTION
1	NC	No contact (For AUO internal use)
2	NC	No contact (For AUO internal use)
3	NC	No contact (For AUO internal use)
4	GND	Power Ground
5	RXIN0-	Negative LVDS differential data input (0)
6	RXIN0+	Positive LVDS differential data input (0)
7	GND	Power Ground
8	RXIN1-	Negative LVDS differential data input (1)
9	RXIN1+	Positive LVDS differential data input (1)
10	GND	Power Ground
11	RXIN2-	Negative LVDS differential data input (2)
12	RXIN2+	Positive LVDS differential data input (2)
13	GND	Power Ground
14	RXCLKIN-	Negative LVDS differential clock input (clock)
15	RXCLKIN+	Positive LVDS differential data input (clock)
16	GND	Power Ground
17	RXIN3-	Negative LVDS differential data input (3)
18	RXIN3+	Positive LVDS differential data input (3)
19	GND	Power Ground
20	NC	No contact (For AUO internal use)
21	NC	No contact (For AUO internal use)
22	NC	No contact (For AUO internal use)
23	GND	Power Ground
24	GND	Power Ground
25	GND	Power Ground
26	VCC	+5V power supply
27	VCC	+5V power supply
28	VCC	+5V power supply
29	VCC	+5V power supply
30	vcc	+5V power supply

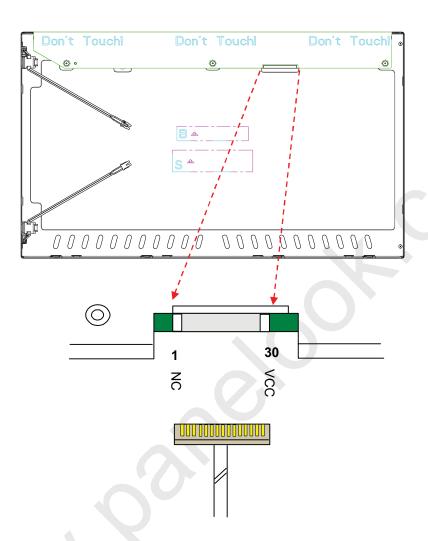
Note1: Start from left side





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Note2: Input signals of clock shall be the same timing.

Note3: Please follow TV VESA Pin Assignment.

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6.4 Timing Characteristics

Basically, interface timing described here is not actual input timing of LCD module but close to output timing of SN75LVDS82DGG (Texas Instruments) or equivalent.

Signal	Item	Symbol	Min	Тур	Max	Unit
	Period	Tv	776	808	1023	Th
	Active	Tdisp(v)	768	768	768	Th
V-section	Blanking	Tblk(v)	8	40	255	Th
	Period	Th	1416	1606	2047	Tclk
	Active	Tdisp(h)	1366	1366	1366	Tclk
H-section	Blanking	Tblk(h)	50	240	681	Tclk
	Period	Tclk	ı	12.8	-	ns
Clock	Frequency	Freq	-	78	90	MHz
Frame Rate	Frame Rate	F	50	60	75	Hz

Note1: DE mode only

Note2: Clock Frequency 90MHz(Max.)= 1416(H)*847(V)*75Hz





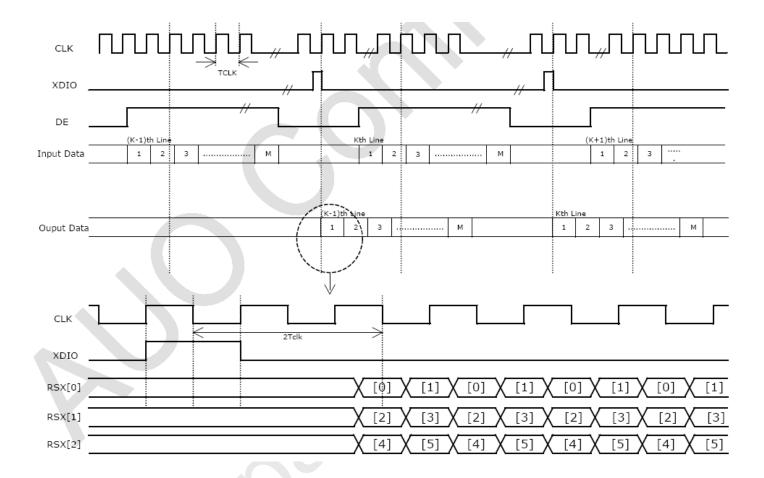
Global LCD Panel Exchange Center

Product Specification

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6.5 Timing diagram





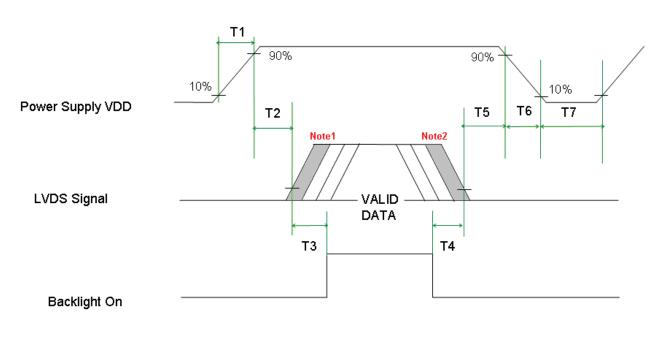


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6.6 Power ON/OFF Sequence

VDD power and lamp on/off sequence are as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



* Note1 : Black Pattern * Note2 : White Pattern

D		Value				
Parameter	Min.	Тур.	Max.	Unit		
T1	0.5	-	10	[msec]		
T2	0	40	50	[msec]		
Т3	200	-	-	[msec]		
T4	200	-	-	[msec]		
T5	0.5	16	50	[msec]		
T6	-	-	100	[msec]		
Т7	1000	_	_	[msec]		





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7.0 Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

Connector Name / Designation	Interface Connector / Interface card		
Manufacturer	JAE or STM		
Type Part Number	FI-XB30SSRL-HF16(JAE)/MSCKT2407P30HB(STM)		
Mating Housing Part Number	FI-X30HL(JAE) (Locked Type) FI-X30H (JAE)(Unlocked Type)		

7.1.1 Pin Assignment

Pin#	Signal Name	Pin#	Signal Name
1	NC	2	NC
3	NC	4	GND
5	RXIN0-	6	RXIN0+
7	GND	8	RXIN1-
9	RXIN1+	10	GND
11	RXIN2-	12	RXIN2+
13	GND	14	RXCLKIN-
15	RXCLKIN+	16	GND
17	RXIN3-	18	RXIN3+
19	GND	20	NC
21	NC	22	NC
23	GND	24	GND
25	GND	26	VCC
27	VCC	28	VCC
29	VCC	30	VCC





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7.2 Backlight Unit

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

Connector Name / Designation	Lamp Connector / Backlight lamp
Manufacturer	YEON HO
Type Part Number	35001HS-02L
Mating Type Part Number	35001WR-02LP

7.2.1 Signal for Lamp connector

	Connector No.	Pin No.	Input	Color	Function
	CN1	1	Hot1	Pink	High Voltage (Lamp1)
Upper	CIVI	2	Cold1	White	Low Voltage(Lamp1)

	Connector No.	Pin No.	Input	Color	Function
	wer CN2	1	Hot1	Pink	High Voltage(Lamp2)
Lower		2	Cold1	White	Low Voltage (Lamp2)





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8.0 Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition		
Temperature Humidity Bias (THB)	Ta= 50°C, 80%RH, 300hours		
High Temperature Operation (HTO)	Ta= 50℃, 50%RH, 300hours		
Low Temperature Operation (LTO)	Ta= 0°C, 300hours		
High Temperature Storage (HTS)	Ta= 60°ℂ , 300hours		
Low Temperature Storage (LTS)	Ta= -20°C, 300hours		
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)		
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)		
Drop Test	Height: 60 cm, package test		
Thermal Shock Test (TST)	-20°C/30min, 60°C/30min, 100 cycles	1	
On/Off Test	On/10sec, Off/10sec, 30,000 cycles		
ESD (Electro Statio Discharge)	Contact Discharge: ± 8KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point.	2	
ESD (Electro Static Discharge)	Air Discharge: \pm 15KV, 150pF(330 Ω) 1sec 8 points, 25 times/ point.		
Altitude Test	Operation:10,000 ft Non-Operation:30,000 ft		

Note 1: The TFT-LCD module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20° C to 60° C, and back again. Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 2: EN61000-4-2, ESD class B: Certain performance degradation allowed

No data lost

Self-recoverable

No hardware failures.



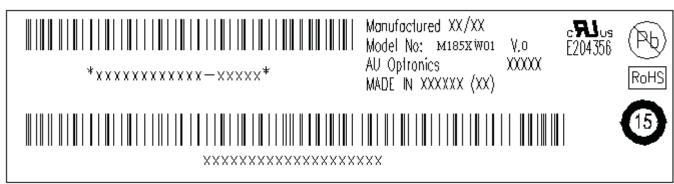


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9.0 Shipping Label

The label is on the panel as shown below:



- Note 1: For Pb Free products, AUO will add 🕒 for identification.
- Note 2: For RoHS compatible products, AUO will add RoHS for identification.
- Note 3: For China RoHS compatible products, AUO will add for identification.
- **Note 4:** The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

